



Europass Curriculum Vitae



Personal information

First name(s) / Surname

Dana - Mihaela VILCU

Address

Spiru Haret University, Faculty of Engineering, Informatics and Geography, 46G Fabricii Str., Sector 6, Bucharest, Romania

E-mail

d.vilcu.mi@spiruharet.ro

Nationality

Romanian

Date of birth

09.01.1972

Family status

married, 3 children (born 2003, 2006, 2010)

Work experience

Dates

from 2009

Occupation or position held

Lecturer

Main activities and responsibilities

course and seminar teaching: *Fundamentals of programming, Algorithms and programming, Formal languages and automata, Data structures, Artificial intelligence, Calculability and complexity, Expert systems, Architectures for mobile applications*; seminar teaching: *Cryptography and security*; university degree theses advising

Name and address of employer

Spiru Haret University, Faculty of Engineering, Informatics and Geography, 46G Fabricii Str., Sector 6, Bucharest, Romania; <http://spiruharet.ro/>

Type of business or sector

teaching, research

Dates

2007 - 2009

Occupation or position held

Scientific researcher

Main activities and responsibilities

administration of scientific projects, research

Name and address of employer

S.C. IPA S.A., 169 Calea Floreasca, P1, 014459 Bucharest, Romania; <http://ipa.ro/>

Type of business or sector

Research & development, engineering and manufacturing for automation equipment and systems

Dates

2001 - 2003

Occupation or position held

ATER (temporary assigned for teaching and research)

Main activities and responsibilities

research & laboratory teaching: real time, computer architecture, C language

Name and address of employer

Université Paris-Est Créteil, Val de Marne, 61 avenue du Général de Gaulle, 94010 Créteil Cedex, France; <http://www.u-pec.fr/>

Type of business or sector

teaching, research

Dates

2000 - 2001

Occupation or position held

PRAG (agreed professor)

Main activities and responsibilities

laboratory teaching: real time, computer architecture, C language

Name and address of employer

Université Paris-Est Créteil, Val de Marne, 61 avenue du Général de Gaulle, 94010 Créteil Cedex, France; <http://www.u-pec.fr/>

Type of business or sector

teaching

Dates

1999 - 2001

Occupation or position held

computer network assistant administrator

Main activities and responsibilities

computer network supervising and maintenance; user assistance; data base application development

Name and address of employer

Institut des Hautes Études Scientifiques, Le Bois-Marie, 35 route de Chartres, 91440 Bures-sur-Yvette, France; <http://www.ihes.fr/jsp/site/Portal.jsp>

Type of business or sector	research
Dates	Feb – Oct 1999
Occupation or position held	French government fellowship
Main activities and responsibilities	MPEG file analyser software development
Name and address of employer	Compagnie de Signaux Technologie Informatique, Bron, France
Type of business or sector	research, developing
Dates	1997 - 1998
Occupation or position held	temporary assistant
Main activities and responsibilities	laboratory teaching: neural networks
Name and address of employer	Universitatea București, Facultatea de Matematică și Informatică, Str. Academiei Nr. 14, C.P. 010014, București, România; http://www.fmi.unibuc.ro
Type of business or sector	teaching, research
Dates	1997 - 1998
Occupation or position held	analyst programmer
Main activities and responsibilities	software porting, programming, testing; quality assurance responsible
Name and address of employer	Kepler Rominfo, 3 Bd. Dimitrie Pompei, 020335 Bucharest, Romania; http://www.kepler.ro
Type of business or sector	software developing and services
Dates	1995 - 1996
Occupation or position held	analyst programmer
Main activities and responsibilities	data base application development
Name and address of employer	Centrul de Calcul al Marinei Române, Constanța, Romania
Type of business or sector	research, development
Education and training	
Dates	1999 - 2004
Title of qualification awarded	PhD in Informatics, with the thesis "Real time embedded systems. CPU power aware task scheduling"; advisor MCF Dr. Gilles Mercier
Principal subjects/occupational skills covered	real time, operating systems, CPU power aware consumption
Name and type of organisation providing education and training	Université Paris XII – Val de Marne, Créteil, Franța; http://www.u-pec.fr/
Dates	1996 - 1997
Title of qualification awarded	MSc in Informatics, "Neural networks in principal component analysis", advisor Prof. Dr. Luminița State
Principal subjects/occupational skills covered	pattern recognition, concurrent programming, distributing systems
Name and type of organisation providing education and training	University of Bucharest, Faculty of Mathematics and Informatics, Operating Systems and Parallel Calculus Section; http://www.fmi.unibuc.ro
Dates	1990 - 1995
Title of qualification awarded	BSc in Informatics, "Possibility theory. Synapsis modelling", advisor Prof. Dr. Luminița State
Principal subjects/occupational skills covered	algorithms and data structures, operating systems, artificial intelligence, data bases, parallel calculus, neural networks, expert systems, LISP, computer networks Psychology, pedagogy, pedagogical skills
Name and type of organisation providing education and training	University of Bucharest, Faculty of Mathematics and Informatics; http://www.fmi.unibuc.ro
Personal skills and competences	

Mother tongue Romanian

Other language(s)

Self-assessment

European level (*)

French

English

Understanding				Speaking				Writing	
Listening		Reading		Spoken interaction		Spoken production		Writing production	
C1	Experienced user	C1	Experienced user	B2	Independent user	C1	Experienced user	B2	Independent user
B2	Independent user	B2	Independent user	B1	Independent user	B2	Independent user	B2	Independent user

(*) [Common European Framework of Reference for Languages](#)

Social skills and competences

Adapting capacity for multicultural environment, acquired through long living experience in other countries (France, Japan)

Organisational skills and competences

“Leadership and team working” certificate (2007), agreed by Labour Ministry and Education Ministry;
“Project manager” certificate (2007), agreed by Labour Ministry and Education Ministry;
“Quality system management” (2012), agreed by Labour Ministry and Education Ministry

Annexes

Scientific activity

Scientific activity

Dana – Mihaela VÎLCU

PhD thesis:

Real time embedded systems. CPU power aware task scheduling, 2004, Univ. Paris XII Val-de-Marne, Faculty of Science and Technologies, France, <http://doxa.u-pec.fr/theses/th0211370.pdf>.

Papers in conference proceedings:

- **D. Vîlcu**, *eLearning and mobile apps in palliative care*, Quality and efficiency in e-learning, Proc. 12th Intl. Sc. Conf. "eLearning and Software for Education" Bucharest, April 21-22, 2015, Carol I NDU Publishing House 10.12753/2066-026X-16-057, pp. 400-404, <http://proceedings.elseconference.eu/>; *ISI Web Of Science Proceedings*
- **D. Vîlcu**, E. Sabău, G. Niculescu, *ICT for sport professionals. From university curricula to sport training and back*, Quality and efficiency in e-learning, Proc. 11th Intl. Sc. Conf. "eLearning and Software for Education" Bucharest, April 23-24, 2015, Carol I NDU Publishing House, 10.12753/2066-026X-15-239, pp. 386-390, <http://proceedings.elseconference.eu/>; *ISI Web Of Science Proceedings*
- **D. Vîlcu**, E. Sabău, G. Niculescu, *E-training mobile devices and applications for sport professionals*, Quality and efficiency in e-learning, Proc. 10th Intl. Sc. Conf. "eLearning and Software for Education" Bucharest, April 24-25, 2014, Ed. Univ. Națională de Apărare "Carol I", pp. 248-253, <http://proceedings.elseconference.eu/>; *ISI Web Of Science Proceedings*
- E. Sabău, G. Niculescu, **D. Vîlcu**, *Virtual Environment Used in the Management of Children Obesity Through Physical Exercises*, Quality and efficiency in e-learning, Proc. 9th Intl. Sc. Conf. "eLearning and Software for Education" Bucharest, April 25-26, 2013, Vol. 3, Ed. Univ. Națională de Apărare "Carol I" (ISSN: 2066 - 026X print 2066 - 8821 online), <http://proceedings.elseconference.eu/>; *ISI Web Of Science Proceedings*
- **D. Vîlcu**, *Real Time Scheduling and CPU Power Consumption in Embedded Systems*, Miclea, L. et al. (eds.), "Proc. 2008 IEEE Intl. Conf. on Automation, Quality and Testing, Robotics (AQTR 2008), 22-25 may 2008, Cluj-Napoca, Romania", Vol. I, 261-266 (2008), IEEE Catalog Number CFP08AQT-PRT, ISBN 978-1-4244-2576-1; <http://ieeexplore.ieee.org/>; *IEEE Xplore - 10.1109/AQTR.2008.4588688*, *ISI Proceedings*
- **D. Vîlcu**, *Power Aware Scheduling on Variable Speed Processor: Limits of the Theoretical Model*, Păltineanu, G. et al. (eds.), "Trends and challenges in applied mathematics. Conference proceedings of the international conference, ICTCAM 2007, Bucharest, Romania, June 20-23, 2007", Matrix Rom. 361-365 (2007), ISBN 978-973-755-283-9; <http://civile.utcb.ro/ictcam/CONTENTS.pdf>; Zentralblatt - **Zbl** pre05507474
- **D. Vîlcu**, *Power Aware Scheduling on Variable Speed Processor*, The 8th Intl. Conf. on Informatics in Economy "Informatics in Knowledge Society", 17-18 may 2007, Bucharest, Romania, Ed. Economică, 1001-1006 (2007), ISBN 978-973-594-921-1; <http://conferenceie.ase.ro/arhive/2007/index.asp>
- **D. Vîlcu**, G. Mercier, *A New Approach to Minimize the Energy Consumption of Hard Real Time Embedded Systems*, Radu, M. (ed. coord.), „International Conference „Communications 2002”: Bucharest, 05-07 December 2002: proceedings”, Ed. Academiei Tehnice Militare, 47-52 (2002), ISBN 973-8290-67-8
- G. Mercier, **D. Vîlcu**, S. Chebira, *Real Time Wireless Transmission of Medical Images Using Predictive Control Based on CMAC Neural Networks*, Radu, M. (ed. coord.), „International Conference „Communications 2002”: Bucharest, 05-07 December 2002: proceedings”, Ed. Academiei Tehnice Militare, 117-122 (2002), ISBN 973-8290-67-8
- G. Mercier, **D. Vîlcu**, *Predictive Control Based on CMAC Networks for Color Video Transmission in a Wireless LAN*, CAIM 2001, The 9th Intl. Conf. on Appl. and Indust. Math., oct. 2001, Pitești, România
- G. Mercier, **D. Vîlcu**, L. George, *Real-Time Application on a CMAC Neural Network*, Dagli, C. et al. (eds.), „Intelligent Engineering Systems Through Artificial Neural Networks: Volume 11 - Smart Engineering System Design”, Proceedings of Artificial Neural Networks in Engineering Conference (ANNIE 01), November 4-7, 2001, St. Louis, Missouri, ASME Press, 477-484 (2001), ISBN 0791801764 - categoria **B**

Journal papers:

- G. Mercier, **D. Vîlcu**, *Multiprocessor Scheduling Theory. A Short Survey*, Analele Universității Spiru Haret, Seria Matematică-Informatică, Anul V, Nr. 5, 2009, 63-73, ISSN 1841-7883; http://anale-mi.spiruharet.ro/archive.html#v5_2009_i1; CNCSIS categ. **B+**
- **D. Vîlcu**, *An Overview on Real Time Scheduling*, Revista Română de Automatică, Vol. XXII, Nr. 1, 2009, 56-64, ISSN 1454-9077; http://ipa.ro/rra/1_2009/continut1_2009.pdf
- **D. Vîlcu**, *CPU Power Consumption in Relation to Scheduling, Real Time Kernel Features, and Application Implementation*, Buletin Științific Univ. Pitești, Seria Matematică și Informatică, Nr. 12 (2006), 1-16, ISSN 1453-116X; http://www.upit.ro/upload/fisiere/19/Stiri/Buletin_2006_195.pdf; CNCSIS categ. **B**
- **D. Vîlcu**, *Neural Network RLS Implementation*, Analele Universității București, Secțiunea Informatică, XLVII 1/1998, 3–12, CNCSIS categ. **C**
- **D. Vîlcu**, *Principal Component Analysis Using the RLS Algorithm. From Theory to a Parallel Implementation*, Analele Universității București, Secțiunea Informatică, XLVII 1/1998, 103–109, CNCSIS categ. **C**, MathSciNet - MR1796463

Talks at conferences without proceeding:

- *Building Apps for Person Care*, Annual Conference on Applied Mathematics and Informatics – CAMAI, iun. 2015, Bucharest, Romania, <http://camai.spiruharet.ro/>
- *Being mobile in bachelor computer science curricula*, Annual Conference on Applied Mathematics and Informatics – CAMAI, jun. 2014, Bucharest, Romania, <http://camai.spiruharet.ro/>
- *CPU Consumption, from Real-Time Embedded Systems to Mobile Devices*, Annual Conference on Applied Mathematics and Informatics – CAMAI, 7-9 jun. 2013, Bucharest, Romania, <http://camai.spiruharet.ro/>
- *Parallel Architectures for Features Extraction Using the RLS Algorithm*, PARADIS 97, Summer School on Parallel and Distributed Systems, 1997, Mangalia, România
- *Modelarea transferului informației la nivel sinaptic*, Prima Conferință Națională de Științe Cognitive, 1995, Ilieni, România

Other talks:

- *Essential programming tools for the mobile Web*, Hour of Code, Fac. Matematică și Informatică, Univ. Spiru Haret, dec. 2015
- *Introducere în programarea aplicațiilor mobile. MIT AppInventor*, Cercul de Informatică, Fac. Matematică și Informatică, Univ. Spiru Haret, nov. 2014
- *Istoria aplicațiilor mobile*, Seminar ISMA - Istoria Statisticii și Matematicii Aplicate, Fac. Matematică și Informatică, Univ. Spiru Haret, nov. 2014
- *Seminar de criptanaliză – Alan Turing, prelegere introductivă*, Fac. Matematică și Informatică, Univ. Spiru Haret, apr. 2014
- *Developing Mobile Applications with App Inventor, Creating Interactive New Media Art*, module du projet Google CS4HS (Computer Science for High School), project manager MCF. dr. A. Olteanu, Prof. dr. G. Albeanu
- *Centenarul Alan Turing*, Seminar ISMA - Istoria Statisticii și Matematicii Aplicate, Fac. Matematică și Informatică, Univ. Spiru Haret, nov. 2012
- *Power aware scheduling*, Univ. Toulouse Jean Jaurés, Institut Universitaire de Technologie Blagnac, 2002